



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

[Handwritten signature]

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/405,237	09/23/1999	JOHN K. RENWICK	IBN-0014	9267
24739	7590	03/22/2004	EXAMINER	
CENTRAL COAST PATENT AGENCY PO BOX 187 AROMAS, CA 95004			MOLINARI, MICHAEL J	
			ART UNIT	PAPER NUMBER
			2665	

DATE MAILED: 03/22/2004

21

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/405,237

Applicant(s)

RENWICK ET AL.

Examiner

Michael J Molinari

Art Unit

2665

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 25 February 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1, 3, 4, 9, 10, 12-15 and 26-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 3, 4, 9, 10, 12-15, and 26-28 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claim 10 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Claim 10 recites the limitation that the logical operation is performed on an address field in the packet. However, claim 10 depends from claim 1, which recites performing a logical operation on a protocol field of the packet. The specification of the instant application explains that the logical operation can be performed on either the address field or the protocol field, but fails to disclose performing the logical operation on both fields. As such, claim 10, which claims performing logical operations on both fields, is not properly enabled by the specification.

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claims 12 and 26 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Lines 14-15 of claim 12 and lines 16-17 of claim 26 recite the limitation that the response signal “includes a label word which defines a plurality of bits”,

Art Unit: 2665

which is unclear. Normally, in the art, pluralities of bits define label words, not the other way around.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1, 3-4, and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rekhter et al. ("Tag Switching Architecture") in view of Davie et al. ("Explicit Route Support in MPLS"), further in view of Semeria ("Multiprotocol Label Switching: Enhancing Routing in the New Public Network").

1. Referring to claim 1, Rekhter et al. disclose a method of forwarding data over a network from a source node to a destination node, comprising: providing a subnetwork (domain, see page 4, column 1, lines 30-31) within the network having a label-switched network (see page 1, column 2, lines 19-22) and a plurality of subnetwork nodes (switches, see page 2, column 1, lines 14-16) connected by a plurality of subnetwork links (see page 5, column 1, lines 22-24), the subnetwork nodes including an ingress node (see page 4, column 2, lines 13-16) and an egress node (see page 4, column 2, lines 16-21) coupled to the source node and the destination node, respectively, at least one pair of subnetwork nodes being connected by a plurality of subnetwork links, the plurality of subnetwork nodes and the plurality of subnetwork links defining a plurality

Art Unit: 2665

of subnetwork paths (routes, see page 2, column 2, lines 29-30) between the ingress node and the egress node; and associating each packet of data to be transferred from a particular source node to a particular destination node with one of the plurality of paths between the ingress node and the egress node (see page 2, lines 13-32); and performing a logical operation on information carried in each packet of data, wherein the logical operation is performed on a protocol field in the packet of data (see page 2, column 2, lines 16-17 and page 5, column 1, lines 38-46 and column 2, lines 1-43. Note that it is well known in ATM to modify the VPI/VCI fields in ATM cell headers as they are passed from switch to switch). Rekhter et al. disclose the use of explicit routes in MPLS but differ from claim 1 in that they do not teach the details of how explicit routes in MPLS work. However, the use of explicit routes in MPLS is well known in the art. For example, Davie et al. disclose forwarding a signal (RSVP path message, see page 3, lines 24-25) from the ingress node (first node, see page 3, lines 18-20) to the egress node (last node, see page 3, lines 18-20) along a route through a subset of subnetwork nodes (the subset of subnetwork nodes is made up of the nodes of the ER-LSP, see page 3, lines 32-36) between the ingress node and the egress node, said signal requesting a response from each node along the route (see page 3, lines 33-34); and receiving response signals from the nodes along the route (the response signals are contained within the RESV message, see page 3, lines 37-40 and page 4, lines 1-6), the response signals defining a path within the route between the ingress node and the egress node as being how explicit routing works in a tag switching (MPLS) network. One skilled in the art would have recognized the advantage of explicit routes as taught by Davie et al. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the implementation of explicit routes in MPLS as taught by Davie et al. into the

Art Unit: 2665

MPLS method of Rekhter et al. to achieve the advantage of implementing explicit routes (which have the advantage of enabling ISPs to have greater control over QoS in their networks).

Rekhter et al. in view of Davie et al. differ from claim 1 in that they fail to disclose the creation of multiple paths between the ingress node and the egress node. However, this is also well known in the art. For example, Semeria teaches the provision of multiple LSPs between each pair of edge LSRs (see page 16, lines 23-26), which has the advantage of providing better QoS in the network. One skilled in the art would have recognized the advantage of provisioning multiple LSPs between each pair of edge LSRs as taught by Semeria. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the use of multiple LSPs between each pair of LSRs as taught by Semeria into the invention of Rekhter et al. in view of Davie et al. to achieve the advantage of providing better QoS in the network.

2. Referring to claim 3, Rekhter et al. disclose that the network comprises nodes (switches) which forward data using Internet protocol node addresses (see page 1, column 2, lines 4-9 and page 3, column 1, lines 8-12).

3. Referring to claim 4, Davie et al. disclose that each subnetwork node along the route allocates a plurality of labels for the plurality of paths along the route (see page 3, lines 39-40 and page 4, lines 1-2. Note that each path has its own label. Therefore, a plurality of paths inherently includes a plurality of labels).

4. Referring to claim 10, Semeria discloses that the logical operation is performed on an address field in the packet of data (see page 4, lines 21-26).

Art Unit: 2665

5. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Rekhter et al. ("Tag Switching Architecture") in view of Davie et al. ("Explicit Route Support in MPLS"), further in view of Semeria ("Multiprotocol Label Switching: Enhancing Routing in the New Public Network") as applied to claim 1 above, and further in view of Woodcock et al. ("Microsoft Press Computer Dictionary").

6. Referring to claim 9, Rekhter et al. disclose the method of claim 8 above, but fail to disclose that the logical operation comprises a hash function. However, the use of hash functions in accessing tables of data (such as routing tables) is well known in the art. For example, Woodcock et al. teach the use of hashing to find an element in a list, which has the advantage of being highly efficient. One skilled in the art would have recognized the advantage of hashing as taught by Woodcock et al. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the use of hashing as taught by Woodcock et al. into the invention of Rekhter et al. in view of Davie et al., further in view of Semeria to achieve the advantage of making the TIB table lookups highly efficient.

7. Referring to claim 12, Rekhter et al. disclose a method of forwarding data over a network from a source node to a destination node, comprising: providing a subnetwork (domain, see page 4, column 1, lines 30-31) within the network having a plurality of subnetwork nodes (switches, see page 2, column 1, lines 14-16) connected by a plurality of subnetwork links (see page 5, column 1, lines 22-24), the subnetwork nodes including an ingress node (see page 4, column 2, lines 13-16) and an egress node (see page 4, column 2, lines 16-21) coupled to the source node and the destination node, respectively, at least one pair of subnetwork nodes being connected by a plurality of subnetwork links, the plurality of subnetwork nodes and the plurality of

Art Unit: 2665

subnetwork links defining a plurality of subnetwork paths (routes, see page 2, column 2, lines 29-30) between the ingress node and the egress node; wherein the subnetwork comprises a label-switching network (see page 1, column 2, lines 19-22). Rekhter et al. disclose the use of explicit routes in MPLS but differ from claim 1 in that they do not teach the details of how explicit routes in MPLS work. However, the use of explicit routes in MPLS is well known in the art. For example, Davie et al. disclose forwarding a signal (RSVP path message, see page 3, lines 24-25) from the ingress node (first node, see page 3, lines 18-20) to the egress node (last node, see page 3, lines 18-20) along a route through a subset of subnetwork nodes (the subset of subnetwork nodes is made up of the nodes of the ER-LSP, see page 3, lines 32-36) between the ingress node and the egress node, said signal requesting a response from each node along the route (see page 3, lines 33-34); and receiving response signals from the nodes along the route (the response signals are contained within the RESV message, see page 3, lines 37-40 and page 4, lines 1-6), the response signals defining a path within the route between the ingress node and the egress node as being how explicit routing works in a tag switching (MPLS) network. One skilled in the art would have recognized the advantage of explicit routes as taught by Davie et al. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the implementation of explicit routes in MPLS as taught by Davie et al. into the MPLS method of Rekhter et al. to achieve the advantage of implementing explicit routes (which have the advantage of enabling ISPs to have greater control over QoS in their networks). Davie et al. further disclose that a response signal includes a label word which defines a plurality of data bits, a first subset of the defined data bits being associated with the route between the ingress node and the egress node and a second subset of the defined data bits being associated

Art Unit: 2665

with the plurality of paths within the route. All messages in the network contain label words and data bits, and these can be divided into subsets, and they are associated with the routes and the plurality of paths by being contained in responses that are carried in the same subnetwork as the route and the paths. Rekhter et al. in view of Davie et al. differ from claim 1 in that they fail to disclose the creation of multiple paths between the ingress node and the egress node. However, this is also well known in the art. For example, Semeria teaches the provision of multiple LSPs between each pair of edge LSRs (see page 16, lines 23-26), which has the advantage of providing better QoS in the network. One skilled in the art would have recognized the advantage of provisioning multiple LSPs between each pair of edge LSRs as taught by Semeria. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the use of multiple LSPs between each pair of LSRs as taught by Semeria into the invention of Rekhter et al. in view of Davie et al. to achieve the advantage of providing better QoS in the network.

8. Referring to claim 13, Davie et al. disclose that the data bits of the second subset of the defined data bits are not assigned values by the node that generated the response signal. Certain bits in each packet could be assigned by any of the nodes along the path of the signal.

9. Referring to claims 14-15, it is well known in the art to use a variable number of data bits to determine a variable number of paths, routes, or addresses, and it is also well known that the number of bits required to label N routes is n , where $N = 2$ to the n power.

10. Referring to claim 20, Rekhter et al. disclose an apparatus for forwarding data over a network from a source node to a destination node, comprising: a subnetwork (domain, see page 4, column 1, lines 30-31) within the network having a plurality of subnetwork nodes (switches,

Art Unit: 2665

see page 2, column 1, lines 14-16) connected by a plurality of subnetwork links (see page 5, column 1, lines 22-24), the subnetwork nodes including an ingress node (see page 4, column 2, lines 13-16) and an egress node (see page 4, column 2, lines 16-21) coupled to the source node and the destination node, respectively, at least one pair of subnetwork nodes being connected by a plurality of subnetwork links, the plurality of subnetwork nodes and the plurality of subnetwork links defining a plurality of subnetwork paths (routes, see page 2, column 2, lines 29-30) between the ingress node and the egress node. Rekhter et al. disclose the use of explicit routes in MPLS but differ from claim 20 in that they do not teach the details of how explicit routes in MPLS work. However, the use of explicit routes in MPLS is well known in the art. For example, Davie et al. disclose a communication subsystem within the subnetwork for (i) forwarding a signal (RSVP path message, see page 3, lines 24-25) from the ingress node to the egress node along a route through a subset of subnetwork nodes (the subset of network nodes is made up of the nodes of the ER-LSP, see page 3, lines 32-36) between the ingress node (first node, see page 3, lines 18-20) and the egress node (last node, see page 3, lines 18-20), said signal requesting a response from each node along the route (see page 3, lines 33-34), and (ii) forwarding response signals from the subnetwork nodes along the route (the response signals are contained within the RESV message, see page 3, lines 37-40 and page 4, lines 1-6), the response signals defining a path within the route between the ingress node and the egress node as being how explicit routing works in a tag switching (MPLS) network. One skilled in the art would have recognized the advantage of explicit routes as taught by Davie et al. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the implementation of explicit routes in MPLS as taught by Davie et al. into the

Art Unit: 2665

MPLS method of Rekhter et al. to achieve the advantage of implementing explicit routes (which have the advantage of enabling ISPs to have greater control over QoS in their networks). Davie et al. further disclose that a response signal includes a label word which defines a plurality of data bits, a first subset of the defined data bits being associated with the route between the ingress node and the egress node and a second subset of the defined data bits being associated with the plurality of paths within the route. All messages in the network contain label words and data bits, and these can be divided into subsets, and they are associated with the routes and the plurality of paths by being contained in responses that are carried in the same subnetwork as the route and the paths. Rekhter et al. in view of Davie et al. differ from claim 20 in that they fail to disclose the creation of multiple paths between the ingress node and the egress node. However, this is also well known in the art. For example, Semeria teaches the provision of multiple LSPs between each pair of edge LSRs (see page 16, lines 23-26), which has the advantage of providing better QoS in the network. One skilled in the art would have recognized the advantage of provisioning multiple LSPs between each pair of edge LSRs as taught by Semeria. Therefore, it would have been obvious to a person with ordinary skill in the art at the time of the invention to incorporate the use of multiple LSPs between each pair of LSRs as taught by Semeria into the invention of Rekhter et al. in view of Davie et al. to achieve the advantage of providing better QoS in the network. Rekhter et al. further disclose that the network comprises a label-switching (label-swapping) network (see page 1, column 2, lines 19-22).

11. Referring to claim 27, Davie et al. disclose that the data bits of the second subset of the defined data bits are not assigned values by the node that generated the response signal. Certain bits in each packet could be assigned by any of the nodes along the path of the signal.

Art Unit: 2665

12. Referring to claim 28, it is well known in the art to use a variable number of data bits to determines a variable number of paths, routes, or addresses.

Response to Arguments

5. Applicant's arguments filed 25 February 2004 have been fully considered but they are not persuasive.

6. Referring to claims 12 and 26, Applicant has argued that Davie et al. fail to teach that the label word of the response signal defines a plurality of data bits. However, Davie et al. do teach that the response signal includes a label word, which defines a plurality of data bits. For example, Davie et al. teach inserting a LABEL object into the RESV message. The LABEL object includes a label word, which defines a plurality of data bits.

7. Referring to claims 12 and 26, Applicant has argued that the examiner has failed to address the limitation that the label word of the response signal defines a plurality of data bits. However, the rejection in the previous office action specifically addresses the claimed limitation.

8. Referring to claim 10, Applicant has argued that “if applicant’s specification explains that a logical operation may be performed on either the address field or the protocol field, it only follows that the logical operation may therefore be performed on both fields”. However, this argument is fallacious. If the specification states that the operation may be performed on **either** one field **or** the other, it does not logically follow that it can be performed on both fields.

9. Further referring to claim 10, Applicant has argued that, because the examiner did not make an enablement rejection in previous office actions, “applicant therefore reserves and asserts the right to assume that the claim is enabled.” However, the examiner may introduce new

Art Unit: 2665

grounds of rejection at any time during prosecution of an application. The examiner may not introduce new grounds of rejection in a final office action. However, the office action in which the new grounds of rejection were introduced was non final and, therefore, the new grounds of rejection were properly introduced.

10. Referring to claims 12 and 26, Applicant has argued that it is clear that a label word defines a plurality of bits because the specification states “the response signal includes a label word which defines a plurality of data bits...”. However, copying language verbatim from the specification does not render a claim definite.

11. Referring to claims 12 and 26, Applicant has argued that since the Examiner made an art rejection of claims 12 and 26, that the claims must be clear and therefore the 112 rejection must be improper. However, it is absolutely correct to make a 112 rejection in addition to an art rejection on a claim.

12. Referring to claim 1, Applicant has argued that because the examiner cited the same references and the same portions of the references as in a previous rejection that it is improper to reject a claim that was previously objected to. However, as indicated above, the examiner can introduce new claim rejections at any time during prosecution, even using the same references. Therefore, such a rejection is not improper.

13. Applicant has argued that, because the allowance of claims 12-15 and 26-28 and the objection to claim 11 were not explicitly withdrawn, that the rejection of said claims is improper. However, this is not so. The rejection of the claims in the previous office action was proper.

14. Applicant has argued that claim 9 is allowable because it depends from allowable claim

1. However, as shown above, claim 1 is rejected. Therefore, claim 9 is not allowable.

Art Unit: 2665

15.

Conclusion

16. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael J Molinari whose telephone number is (703) 305-5742. The examiner can normally be reached on Monday-Thursday 8am-6:30pm.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy Vu can be reached on (703) 308-6602. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2665

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Michael Joseph Molinari



ALPUS H. HSU
PRIMARY EXAMINER